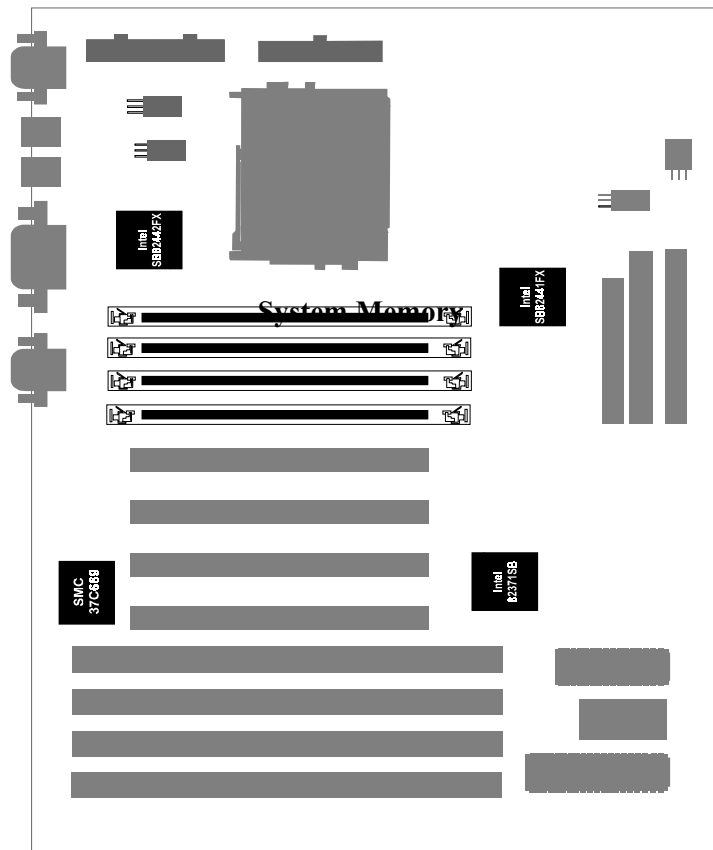


2 Memory Configurations

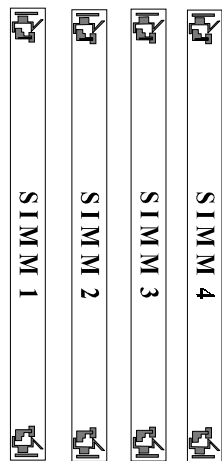
This chapter contains the detailed memory configuration: **System Memory and Cache Memory**.



The diagram above displays the location of SIMM Sockets on P6FX1-A motherboard.

System Memory

Bank 0 Bank 1



SIMM:

P6FX1-A provides tremendous flexibility DRAM configurations. It accepts a maximum 256 MB memory size. The main memory is installed with SIMM (Single-In-line-Memory Module).

There are two memory banks which support the 4M/8M/16M/32M/64M type, single and/or double-density modules. The DRAM type of SIMM0 /SIMM1 is independent of SIMM2/SIMM3.



The type of SIMM 0 and SIMM1 must be same if they exist at the same time.

The following table lists a number of possible DRAM combinations.

Bank 0		Bank 1		Total
SIMM1	SIMM2	SIMM3	SIMM4	Memory Size
4MB	4MB	-----	-----	8MB
8MB	8MB	-----	-----	16MB
16MB	16MB	-----	-----	32MB
32MB	32MB	-----	-----	64MB
64MB*	64MB*	-----	-----	128MB
4MB	4MB	4MB	4MB	16MB
4MB	4MB	8MB	8MB	24MB
4MB	4MB	16MB	16MB	40MB
4MB	4MB	32MB	32MB	72MB
4MB	4MB	64MB*	64MB*	136MB
8MB	8MB	8MB	8MB	32MB
8MB	8MB	16MB	16MB	48MB

Continued.....

P6FX1-A

Bank 0		Bank 1		Total
SIMM0	SIMM1	SIMM2	SIMM3	Memory Size
8MB	8MB	32MB	32MB	80MB
8MB	8MB	64MB*	64MB*	144MB
16MB	16MB	16MB	16MB	64MB
16MB	16MB	32MB	32MB	96MB
16MB	16MB	64MB*	64MB*	160MB
32MB	32MB	32MB	32MB	128MB
32MB	32MB	64MB*	64MB*	192MB
64MB*	64MB*	64MB*	64MB*	256MB

Table 2 -1. System Memory Configurations

* : means the memory size is not available for testing now.



We recommend not to mix the EDO and FPM on the system.

Cache Memory Subsystem

Level 1 Cache

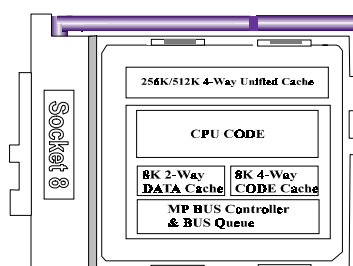
16 KB Level 1 Cache that is built in Pentium Pro CPU includes Data Cache and Code Cache.

- 1. Data Cache: supports 8KB Write-Through and Write-Back policy.
- 2. Code Cache: supports 8KB Write-Through policy.

Level 2 Cache

Level 2 Cache is included in Pentium Pro CPU. The Size of Level 2 Cache is 256KB or 512KB depending on CPU.

Pentium Pro CPU



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